ABSTRACT OF THE DISCLOSURE

A global processor interprets a program and control entirety. A processor-element block includes a plurality of processor elements each comprising a

- 5 register file and an operation array for processing a plurality of sets of data,
 - wherein the global processor outputs a control signal to the plurality of processor elements, and, thereby, sets processor-element numbers corresponding to the plurality
- of processor elements as input values of the operation arrays, respectively.